#### Practitioner's Docket No. MI22-1171

PATENT

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Akram et al., Salman

Application No.: 09/292.132 Filed: 04/14/99

For: Methods of Forming a Transistor Gate

Group No.: 2812

Examiner: S. Mulpuri

CENTRAL FAX CENTER

RECEIVED

DEC 1 1 2003

Assistant Commissioner for Patents Washington, D.C. 20231

# CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

Response to November 6, 2003 Office Action

lecender 8, 2003

Signature

Robin Saldivia

total pages

(Certification of Pacsimile Transmission--page 1 of 1)

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No 09/292,132
Filing Date April 14, 1999
Inventor
Assignee Micron Technology, Inc.
Group Art Unit
Examiner S. Mulpuri
Attorney's Docket No
Title: Methods of Forming A Transistor Gate

## **RESPONSE TO NOVEMBER 6, 2003 OFFICE ACTION**

GENTHAL FAX CENTER

To:

Mail Stop Non-Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

D. Brent Kenady

Tel. 509-624-4276; Fax 509-838-3424

Wells St. John P.S.

601 W. First Avenue, Suite 1300 Spokane, WA 99201-3828

Responsive to the Office Action dated November, 2003, Applicant amends and remarks as follows:

### **AMENDMENTS**

1

Underlines indicate insertions and strikeouts indicate deletions.